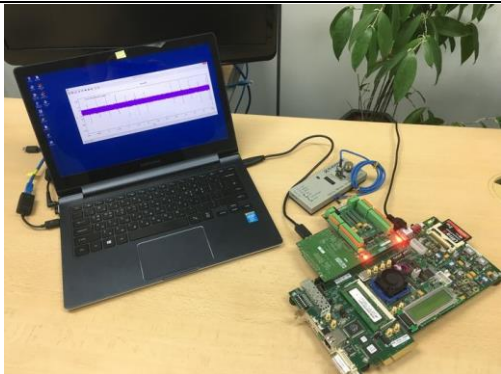
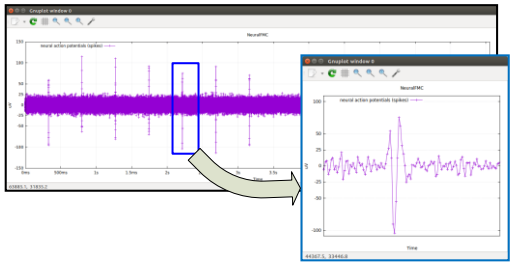


Press Release	
title	Future Design Systems developed Neural Signal Interface System
date	Dec. 29, 2018
company	Future Design Systems, Inc. www.future-ds.com / contact@future-ds.com Faculty Wing F723, KAIST Munji Campus, 193 Mnuji-Ro, Yuseong-Gu, Daejeon 34051, Korea

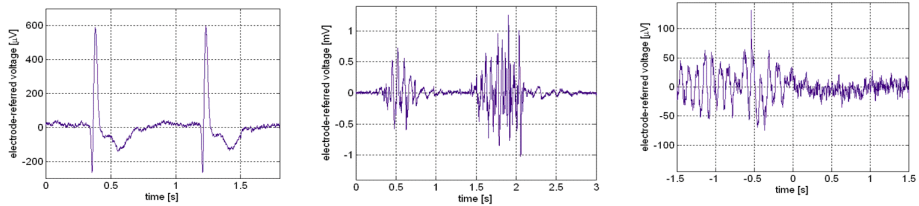
Future Design Systems developed neural signal interface system. This system consisting of NeuralFMC, FPGA board, and CON-FMC gets neural signals through FPGA and USB3.0. Each NeuralFMC board support up to 128 ADC channels.

Future Design Systems has plan to add signal processing and deep-learning feature and apply application areas including AI, HCI, neuro-medical and so on.

	
System in operation	Neural signal example

[Neural signals]

Neural signals are electric signal from μV ~ mV ranges. There are many different types of signals including ECC/EKG, EMG, EEG and so on.



[ANSI/VITA 57.1 FMC]

FPGA Mezzanine Card (FMC) is an ANSI/VITA (VMEbus International Trade Association) 57.1 standard that defines I/O mezzanine modules with connection to an FPGA or other device with re-configurable I/O capability

[FPGA]

Field Programmable Gate Arrays (FPGAs) are semiconductor devices that are based around a matrix of configurable logic blocks (CLBs) connected via programmable interconnects. FPGAs can be reprogrammed to desired application or functionality requirements after manufacturing.

[Future Design Systems]

“Future Design Systems” started its business from June 2017 and focuses on developing product using FPGA in the area of deep-learning accelerator and PCe/USB/Ethernet.

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